IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Group AP20 Rec'G PCT/PTO 16 DEC 2005 Examiner: Mikael Guenais Applicants: Application No.: Date Filed: Intl. Appl. No.: PCT/EP2004/006481 June 16, 2004 Intl. Filing Date: Title: PHASE LOCKED LOOP FILTER INFORMATION DISCLOSURE STATEMENT (IDS) Commissioner For Patents Arlington, VA 22313 SIR: In accordance with 37 C.F.R. §1.56 and in compliance with 37 C.F.R. §§1.97 and 1.98, the references listed on attached Form PTO/SB/08 and/or subsequently identified herein, are being submitted herewith for consideration by the United States Patent and Trademark Office. Pursuant to the Office waiving the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC §371 after June 30, 2003, copies of the reference are not submitted herewith. I. COPIES A legible copy of (i) each U.S. and foreign patents; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed, is included herewith. Ъ.П Any patents, publications or other information which are listed on PTO/SB/08 which are not enclosed herewith were previously cited by or submitted to the PTO in one of the following applications which has been relied upon for an earlier filing date under 35 U.S.C. §120: U.S. Serial Number U.S. Filing Date II. CONCISE EXPLANATION OF THE RELEVANCE (check at least one box) Except as may be indicated below in (b) of this section, all of the patents, publications or other information are in the English language (concise explanation not required). ь. 🔲 A concise explanation of the relevance of all patents, publications or other information listed that is not in the English language is as follows: c. 🔲 The following additional information is provided for the Examiner's consideration: CROSS REFERENCE TO RELATED APPLICATION(S) The Examiner is advised that the following co-pending application(s) contain(s) subject matter that may be related to the present application. By bringing this (these) applications to the Examiner's attention, Applicant(s) does(do) not waive the confidentiality provisions of 35 U.S.C. §122. Serial No. Filing Date Art Unit **FEES** IV. 🔯

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DS IS BEING FILED UNDER 37 C.F.R. §1.97(b): (check one box)
within three months of the filing date of a national application other than a continued prosecution
application under § 1.53(d) (37 C.F.R. §1.97(b)(1)). No fee or statement is required.
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international application (37 C.F.R. §1.97(b)(2)). No fee or statement is required.
before the mailing date of a first Office Action on the merits (37 C.F.R. §1.97(b)(3)). No fee or statement is required.
before the mailing date of a first Office Action after the filing of a request for continued examination under § 1.114 (37 C.F.R. § 1.97(b)(4)). No fee or statement is required.

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V. 🗌	before tunder 3	he mailin 7 C.F.R.	§1.311, or an action that otherw	Action under 37 C.F.R. §1.113, a Notice of Allowance ise closes prosecution in the application (See 37 C.F.R.	
	§1.97(c) a. □ b. □	No state	ment; therefore, charge deposit ac statement below. No fee is requir	ecount 5030 19th 21e serforth in 37 G.F.R. (1. 12(6). DEC 20 ed.	05
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FREES	CALE SI	EMICON	DUCTOR, INC.	Respectfully submitted, Mikael Guenais  Dayld G. Dolezal Anorney for Applicant(s)	,
Custom	er Numb	er 23125		Reg. No. 41,711 Telephone No.: 512.996.6839	
Enclosu	ıres:		PTO/SB/08 References AD & AE Foreign Search Report	Fax No.: 512.996.6853	

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II	NFORMATION DISC	CLOSUR	Œ	Application Number	
ST	ATEMENT BY AP	<b>PLICA</b>	NT	Filing Date	
				First Named Inventor	MIKAEL GUENAIS
				Group Art Unit	
(use as many sheets as necessary)				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	SC12600ET

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Examiner Initials*	Cite No. 1		cument Kind Code <sup>2</sup> (if known)	Name o	f Patentee or Applicant Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	AA	5,424,689	(II KIIOWII)	Gillig et al		06-13-1995	gu.eoppem		
	AB	2004/0101081	A1	Hsu	t ui	05-27-2004			
	AC	5,774,023	- A1	Irwin		06-30-1998			
	AC	3,114,023		II WIII		00-30-1778			
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	AD	CRANINCKX et al; "A Fully Integrated CMOS DCS-1800 Frequency Synthesiser"; IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998, pp 2054-2065							
	AE	CRANINCKX et al; "A Fully Integrated CMOS DCS-1800 Frequency Synthesiser"; IEEE International Solid-State Circuits Conference, pp 372-374							
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